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and second midpoints, and wherein said center of said H-shaped pattern is the midpoint of said third branch.

- 3. (Amended) The network of claim 1 wherein each level is at least one X-shaped pattern comprising first and second branches each having a respective first and second midpoint and Interconnecting said first and second branches at said midpoints, and wherein said center of said X-shaped pattern is the intersection of said first and second branches.
- 4. (Amended) The network of claim 1 wherein said network is located on an optoelectronic chip.
- 5. (Amended) The network of claim 1 wherein said terminal nodes are optoelectronic devices.
- 6. (Amended) The network of claim 1 wherein said terminal nodes are VCSELs.
- 8. (Amended) The network of claim 7 wherein each level is at least one H-shaped pattern comprising first and second parallel branches each having a respective first and second midpoint, and a third branch interconnecting said first and second midpoints, and wherein said center of said H-shaped pattern is the midpoint of said third branch.
- 9. (Amended) The network of claim 7 wherein each level is at least one X-shaped pattern comprising first and second branches each having a respective first and second midpoint and interconnecting said first and second branches at said midpoints, and wherein said center of said X-shaped pattern is the intersection of said first and second branch s.

